

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

1-32. (cancelled)

33. (currently amended) A self test system for a medical device, comprising:

a plurality of components each of which has a respective self-test means associated therewith ~~capable of carrying~~ adapted to carry out a self-test routine on the associated component,

wherein each of said self-test means is ~~operable~~ adapted to be activated independently of operation of the medical device and not by a signal from a processor associated with said medical device, with ~~[[the]]~~ results thereof being passed to a common processor.

34. (currently amended) A self test system for a medical device according to claim 33, wherein the self test is adapted to be activated independently of operation of the medical device and not by a signal from the ~~centralized~~ common processor associated with said medical device.

35. (currently amended) A self test system for a medical device according to claim 33, wherein the self test system includes:

a summator ~~which receives~~ adapted to receive data from the one or more self test ~~units about~~ means associated with said components, the summator storing said data; and one of:

an indicator to which said data ~~so that it~~ can be transmitted ~~to an indicator either~~ directly; or

~~via~~ a processor which can access said data.

36. (currently amended) A self test system for a medical device, said medical device being arranged to transmit information concerning components of said medical device ~~to an indicator which can show the status of the components when tested, characterised in that the self test system comprises~~ comprising:

one or more self test units ~~which can~~ adapted to independently self test one or more individual components of the medical device; and

a summator ~~which receives~~ adapted to receive data from the one or more self test units ~~about~~ associated with said components, the summator storing said data; and one of:

an indicator to which said data ~~so that it~~ can be transmitted ~~to an indicator either~~ directly, the indicator being adapted to show the status of the components when tested; or

~~via~~ a processor which can access said data.

37. (currently amended) A self test system according to claim 33, wherein the ~~one or more~~ plurality of components each has a dedicated self test unit.

38. (currently amended) A self test system according to claim 33, wherein ~~[[a]]~~ one or more of the plurality of ~~the one or more~~ components communicate with a self test unit.

39. (currently amended) A self test system according to claim 33, ~~wherein the data from the self test is fed by~~ further comprising a single data link, wherein the single data link is adapted to feed data from the self test to the a summator.

40. (currently amended) A self test system according to claim 33, wherein ~~[[the]]~~ data from the self test units is fed by a plurality of separate data links to ~~[[the]]~~ a summator.

41. (previously presented) A self test system according to claim 34, wherein the summator is a separate counter/adder component, or a micro-controller.

42. (previously presented) A self test system according to claim 34, wherein the summator is or includes a subtractor component.

43. (currently amended) A self test system according to claim 34, further comprising a main microprocessor, wherein the summator is part of [[a]] the main microprocessor.

44. (currently amended) A self test system according to claim 34, wherein data from ~~the~~<sub>or</sub> each component is delivered to the summator as a signal comprising a number of pulses.

45. (currently amended) A self test system according to claim 44, wherein the pulses are identified as discrete numbers of pulses to ~~the~~ a value of  $x^2$ , where x is a whole number, or as a prime number.

46. (currently amended) A self test system according to claim 44, wherein the self test system comprises an AED, and a number of pulses for the following components making up [[an]] the AED are any of the following:

<del>Electrode</del> <u>electrode</u> condition	$32^2 = 1024$ pulses
<del>Battery</del> <u>battery</u> on charge	$33^2 = 1089$ pulses
<del>System</del> <u>system</u> condition	$34^2 = 1156$ pulses
<del>Modem</del> <u>modem</u> condition	$35^2 = 1225$ pulses.

47. (currently amended) A self test system according to claim 33, wherein a self test for a component is triggered via triggering means by a test having been carried out on another component.

48. (currently amended) A self test system according to claim 33, ~~wherein the test is activated by~~ further comprising a digital signal processor adapted to activate the test.

49. (currently amended) A self test system according to claim 48, further comprising a server of base station, wherein the digital signal processor is activated by a signal from [[a]] the server or base station in contact with the medical device.

50. (currently amended) A self test system according to claim 33, further comprising a base station for said device, wherein the self test is activated by the medical device being placed in [[a]] the base station for said device.

51. (currently amended) A self test system according to claim 33, further comprising circuitry of said device, wherein a self test involves testing ~~the~~ a voltage across substantially all of the circuitry of the medical device.

52. (currently amended) A self test system according to claim 48, wherein the test is carried out either at a first voltage of ~~between~~ 450V or a second voltage of 40V.

53. (currently amended) A self test system according to claim 33, further comprising an indicator and a summator, wherein the indicator is a digital display ~~that can~~ adapted to display results for the summator or processor independently of one another.

54. (previously presented) A self test system according to claim 53, wherein the processor reviews the results of the summator prior to the results being fed to the indicator.

55. (currently amended) A self test system according to claim 53, wherein ~~the~~ a number of pulses being fed to the summator is recorded.

56. (previously presented) A self test system according to claim 55, wherein the number of pulses is measured against set parameters to provide an indication of whether one or more components are functioning as required.

57. (currently amended) A self test system according to claim 33, wherein the self test system tests the ~~condition of the~~

power source for the medical device, prior to testing other components.

58. (previously presented) A self test system according to claim 33, wherein the testing of components is based on testing a sample of signals over time or testing a defined number of signals for each component.

59. (currently amended) A self test system according to claim 33, wherein ~~the~~ an indicator is integral with said medical device.

60. (currently amended) A self test system according to claim 33, wherein ~~the~~ an indicator is a separate component associated with said medical device.

61. (currently amended) A method of self testing a medical device, said medical device being arranged to transmit information concerning components of said medical device to an indicator, wherein comprising:

carrying out a self test of one or more components are  
~~caused to carry out a self test, the~~ independent of operation of  
the medical device;

storing results of the self test; and ~~are stored and~~

on operation of said medical device, transmitting said results ~~are transmitted~~ to a processor for analysis and display by said indicator.

62. (currently amended) A method according to claim 61, ~~wherein~~ further comprising activating a timing device so that the self test is a periodic or aperiodic self test, ~~activated by a timing device.~~

63. (currently amended) A method according to claim 61, ~~wherein~~ further comprising sending information concerning said components ~~is sent~~ to a summator prior to being sent to a processor.